



## 2S7B2\_6UP series

2W - Single/Dual Output DC-DC Converter - Isolated & Unregulated

# DC-DC Converter 2 Watt

- ⊕ SIP7 Package
- ⊕ Ultra-High I/O Isolation  
5000VAC with Reinforced  
Insulation, rated for 300 Vrms  
working voltage
- ⊕ CMTI 65 KV/μs at 1kV  
Common mode voltage
- ⊕ Continuous short circuit  
protection
- ⊕ Low coupling capacitance
- ⊕ Efficiency up to 86%
- ⊕ -40°C ~ 95°C operation  
temperature range
- ⊕ Suitable for IGBT applications

The 2S7B2\_6UP series are miniature, 5kVAC isolated 2W DC-DC-converters in a SIP package with single and dual output voltage. They offer the ideal solution in many space critical applications for board level power distribution. The internal SMD construction makes it possible to offer a product with high performance at low cost. The series offers smaller size, improved efficiency, lower output ripple noise.



Common specifications	
Operating ambient temperature	-40 - +95 °C
Maximum case temperature	+115 °C
Thermal impedance	30 °C/W
Operating altitude	5000 m
Pollution degree	PD2
Storage humidity	95 % rel. H
Storage temperature	-55 - +125 °C
Cooling	Natural Convection / 30-65 LFM
Common mode transient immunity	1kV Common mode Voltage / 65 KV/μs
Leakage current	250VAC, 60Hz / 2.0 μA
Clearance / creepage	6 mm
Working voltage	300 Vrms
Overvoltage category	OV2
Switching frequency	20 kHz
Mtbf (mil-hdbk-217f @25°C):	2.4 M hours
Safety approval	IEC / EN / UL 62368-1 - DK-131558-UL, E347551
Insulation grade	Reinforced Insulation
Environmental compliance	RoHS
Case material	Nonconductive black plastic (UL94V-0 rated)
Pin material	Tinned Copper
Potting material	Silicone ( UL94V-0 rated )
Weight	4.3 g, typ.
Dimensions	0.77" x 0.39" x 0.49"

Isolation specifications					
Item	Test condition	Min	Typ	Max	Units
Isolation Voltage	Input-output, and rated for 60sec	5000			VAC
Isolation Resistance	Input-output	1000			MΩ
Isolation Capacitance	Input-output		10		pF

### Example:

**2S7B2\_1205D6UP**

2 = 2Watt; S7 = SIP7; B2 = Pinning; 12 = 12Vin; 05 = 5Vout; D = Dual Output; 6 = 6kVDC isolation; U = Unregulated Output; P = Short circuit protection

Output specifications					
Item	Test condition	Min	Typ	Max	Units
Output voltage accuracy		-3		+3	%
Line regulation	For 1% Vin Change	-1.2		+1.2	%
Load regulation	From 10% to 100% Load • 3.3V, 5V Output • 9V, 12V, 15V Output From 0% to 100% Load			15 10 35	% % %
Cross regulation	Asymmetrical Load 25% / 100% for Dual Output • 3.3V , 5V Output • Other Output	-8 -5		+8 +5	% %
Ripple & noise	• 20MHz bandwidth.			150	mVpk-pk
Temperature coefficient		-0.03		+0.03	%/°C
Maximum capacitive load	Minimum Vin and constant resistive load	See Table			

Input specifications					
Item	Test condition	Min	Typ	Max	Units
Voltage range	• 5V Input • 12V Input • 15V Input • 24V Input	4.5 10.8 13.5 21.6	5 12 15 24	5.5 13.2 16.5 26.4	VDC VDC VDC VDC
Input filter	Capacitor				
Input reflected ripple current*		20			mApk-pk
Start up time	Nominal Vin and constant resistive load	30			ms
Recommended input fuse (slow blow)	• 5V Input • 12V Input • 15V Input • 24V Input	1 0.4 0.4 0.2	A A A A		
Input Surge Voltage ( 100 ms )	• 5V Input • 12V Input • 15V Input • 24V Input	7 15 18 28			VDC VDC VDC VDC
Soldering Temperature	1.5mm from case 10sec max.	260			°C

Note: \*Measured with a simulated source inductance of 22μH and a source capacitor Cin ( 47μF, ESR<1.0Ω at 100kHz ).

\*\* These are stress ratings. Exposure of devices to any of these conditions may adversely affect long-term reliability.

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### Product Selection Guide

Approval	Part Number	Input voltage [V]	INPUT Current No-Load [mA, max.]	INPUT Current Full Load [mA, typ.]	Output voltage [V]	Output current [Min. load, mA]	Output current [Full load, mA]	Efficiency [%, Typ.]	Capacitive Load (µF, max.)
	2S7B2_0503S6UP	4.5-5.5	50	501.26	3.3	0	600	79	3300
UL	2S7B2_0505S6UP	4.5-5.5	60	506.33	5	0	400	79	1200
	2S7B2_0509S6UP	4.5-5.5	60	493.33	9	0	222	81	1200
UL	2S7B2_0512S6UP	4.5-5.5	60	477.14	12	0	167	84	680
UL	2S7B2_0515S6UP	4.5-5.5	60	480.72	15	0	133	83	680
	2S7B2_1203S6UP	10.8-13.2	25	208.86	3.3	0	600	79	3300
UL	2S7B2_1205S6UP	10.8-13.2	30	200.80	5	0	400	83	1200
	2S7B2_1209S6UP	10.8-13.2	30	200.60	9	0	222	83	1200
UL	2S7B2_1212S6UP	10.8-13.2	30	200.30	12	0	167	83	680
UL	2S7B2_1215S6UP	10.8-13.2	30	200.30	15	0	133	83	680
	2S7B2_1503S6UP	13.5-16.5	25	167.09	3.3	0	600	79	3300
	2S7B2_1505S6UP	13.5-16.5	30	162.60	5	0	400	82	1200
	2S7B2_1509S6UP	13.5-16.5	30	162.44	9	0	222	83	1200
	2S7B2_1512S6UP	13.5-16.5	30	159.04	12	0	167	84	680
	2S7B2_1515S6UP	13.5-16.5	30	160.24	15	0	133	83	680
	2S7B2_2403S6UP	21.6-26.4	20	107.14	3.3	0	600	77	3300
UL	2S7B2_2405S6UP	21.6-26.4	20	101.62	5	0	400	82	1200
	2S7B2_2409S6UP	21.6-26.4	20	100.30	9	0	222	83	1200
UL	2S7B2_2412S6UP	21.6-26.4	20	99.40	12	0	167	84	680
UL	2S7B2_2415S6UP	21.6-26.4	20	100.15	15	0	133	83	680

Approval	Part Number	Input voltage [V]	INPUT Current No-Load [mA, max.]	INPUT Current Full Load [mA, typ.]	Output voltage [V]	Output current [Min. load, mA]	Output current [Full load, mA]	Efficiency [%, Typ.]	Capacitive Load (µF, max.)
	2S7B2_0503D6UP	4.5-5.5	50	493.77	±3.3	0	±303	81	±1500
UL	2S7B2_0505D6UP	4.5-5.5	60	493.82	±5	0	±200	81	±470
	2S7B2_0509D6UP	4.5-5.5	60	481.44	±9	0	±111	83	±470
UL	2S7B2_0512D6UP	4.5-5.5	60	480.00	±12	0	±83	83	±330
UL	2S7B2_0515D6UP	4.5-5.5	60	490.24	±15	0	±67	82	±330
UL	2S7B2_051509D6UP	4.5-5.5	60	495.64	+15 / -9	0	+67 / -111	83	±330
	2S7B2_1203D6UP	10.8-13.2	25	203.23	±3.3	0	±303	82	±1500
UL	2S7B2_1205D6UP	10.8-13.2	30	198.41	±5	0	±200	84	±470
	2S7B2_1209D6UP	10.8-13.2	30	200.60	±9	0	±111	83	±470
UL	2S7B2_1212D6UP	10.8-13.2	30	197.62	±12	0	±83	84	±330
UL	2S7B2_1215D6UP	10.8-13.2	30	197.06	±15	0	±67	85	±330
UL	2S7B2_121509D6UP	10.8-13.2	30	196.03	+15 / -9	0	+67 / -111	85	±330
	2S7B2_1503D6UP	13.5-16.5	25	164.59	±3.3	0	±303	81	±1500
	2S7B2_1505D6UP	13.5-16.5	30	160.64	±5	0	±200	83	±470
	2S7B2_1509D6UP	13.5-16.5	30	158.57	±9	0	±111	84	±470
	2S7B2_1512D6UP	13.5-16.5	30	154.41	±12	0	±83	86	±330
	2S7B2_1515D6UP	13.5-16.5	30	155.81	±15	0	±67	86	±330
	2S7B2_151509D6UP	13.5-16.5	30	158.69	+15 / -9	0	+67 / -111	84	±330
	2S7B2_2403D6UP	21.6-26.4	20	104.15	±3.3	0	±303	80	±1500
UL	2S7B2_2405D6UP	21.6-26.4	20	99.20	±5	0	±200	84	±470
	2S7B2_2409D6UP	21.6-26.4	20	99.10	±9	0	±111	84	±470
UL	2S7B2_2412D6UP	21.6-26.4	20	97.64	±12	0	±83	85	±330
UL	2S7B2_2415D6UP	21.6-26.4	20	98.53	±15	0	±67	85	±330
UL	2S7B2_241509D6UP	21.6-26.4	25	100.37	+15 / -9	0	+67 / -111	83	±330

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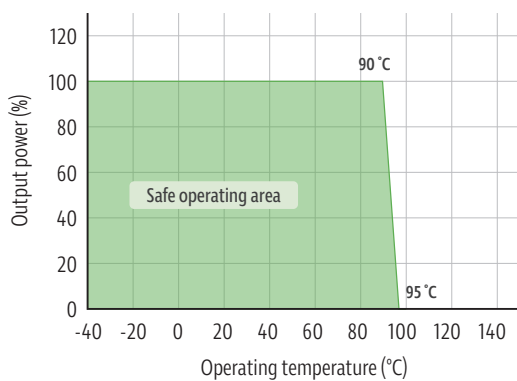
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### EMC specifications (EN50155)

Conducted Emissions	EN55032	with external components	Perf. Criteria B
Radiated Emissions	EN55032		Perf. Criteria B
ESD	IEC 61000-4-2	Air $\pm 15\text{kV}$ / Indirect: $\pm 6\text{kV}$	Perf. Criteria A
RS	IEC 61000-4-3	10V/m	Perf. Criteria A
EFT	IEC 61000-4-4	$\pm 2\text{kV}$ with external components	Perf. Criteria A
Surge	IEC 61000-4-5	$\pm 2\text{kV}$ with external components	Perf. Criteria A
CS	IEC 61000-4-6	10Vrms	Perf. Criteria A
PFMF	IEC 61000-4-8	100A/m	Perf. Criteria A

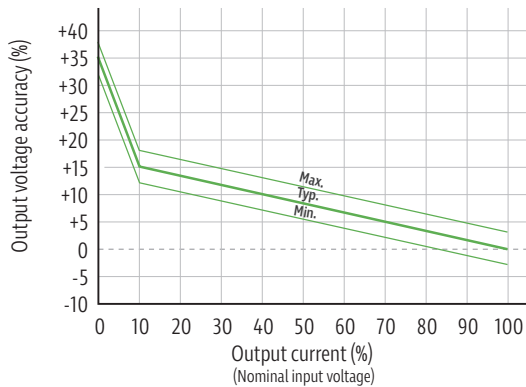
## Typical characteristics

### Temperature derating graph



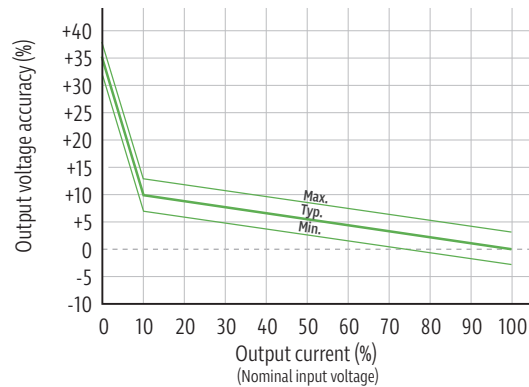
### Output regulation curve

3.3 - 5V output

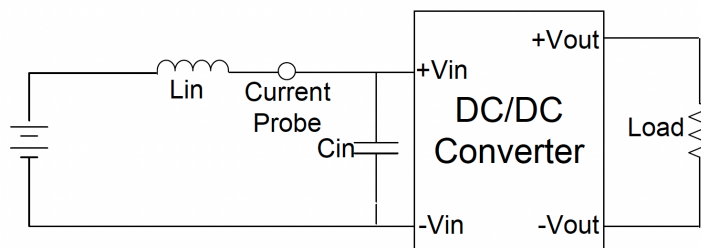


### Output regulation curve

9 - 12 - 15V output



## Input reflected ripple current test step

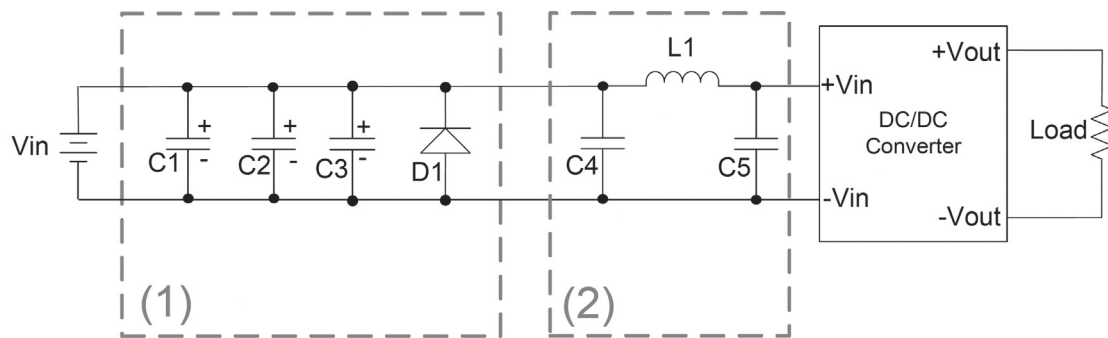


Input reflected ripple current is measured with a source inductor  $L_{in}$  (22 $\mu\text{H}$ ) and a source capacitor  $C_{in}$  (47 $\mu\text{F}$ , ESR<1.0 $\Omega$  at 100kHz) at nominal input and full load.

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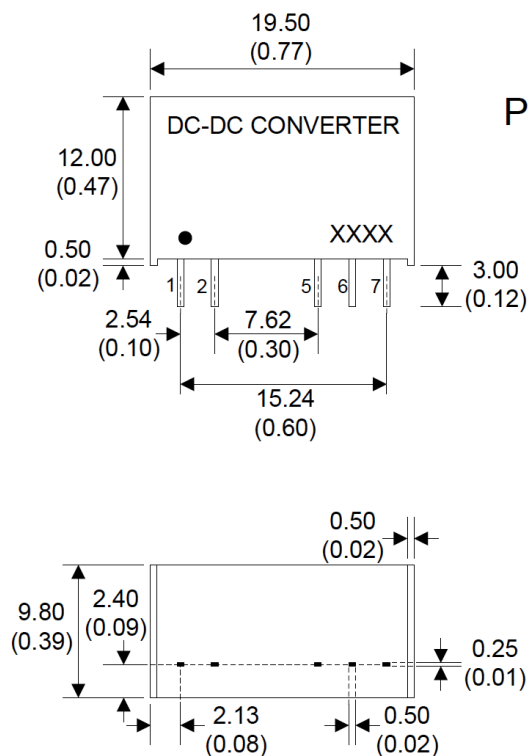
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### EMC filter

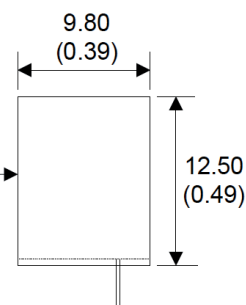


Vin	C1, C2	C3	D1	C4	L1	C5
5V	NIPPON Chemi-con KY Series 680μF, 100V	NIPPON Chemi-con KY Series 680μF, 100V	SMDJ8.0A	MLCC 4.7μF, 50V	6.8μH	DNP
12V		DNP	SMDJ16.0A	MLCC 10μF, 50V	6.8μH	MLCC 4.7μF, 50V
15V		DNP	SMDJ18.0A	MLCC 10μF, 50V	6.8μH	MLCC 4.7μF, 50V
24V		DNP	SMDJ28.0A	MLCC 10μF, 50V	6.8μH	MLCC 4.7μF, 50V

### Mechanical dimensions



Printed Face



7 PIN SIP Dual Output		
Pin	Single	Dual
1	+Vin	+Vin
2	-Vin	-Vin
5	-Vout	-Vout
6	N.P.	COM
7	+Vout	+Vout

\*N.P.: No PIN

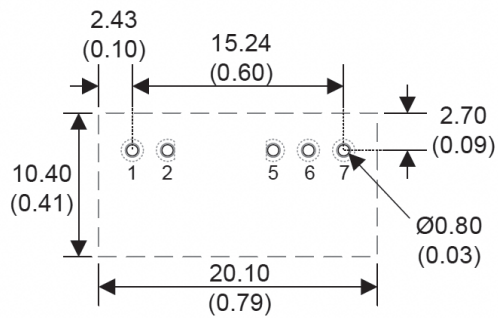
Notes: All dimensions are typical in millimeters (inches).

1. Pin dimension tolerance:  $\pm 0.05$  ( $\pm 0.002$ )
2. Pin pitch and length tolerance:  $\pm 0.35$  ( $\pm 0.014$ )
3. Case tolerance:  $\pm 0.5$  ( $\pm 0.02$ )
4. Pin to Case tolerance:  $\pm 0.5$  ( $\pm 0.02$ )

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### Recommended footprint details



Notes: 1. All dimensions are typical in mm (inches).

Through hole 1 ~ 7: Ø0.80 (0.031)

Top view pad 1 ~ 7: Ø1.00 (0.039)

Bottom view pad 1 ~ 7: Ø1.60 (0.063)

pad 2 to pad 5 spacing: 6.00 (0.236)

2. There should be at least 6mm distance between primary and secondary circuit.